

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	127	clevenger-law\$.IN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:26
L2	231	clevenger-l\$.In.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:27
L3	2	2 and batter\$3 and ((integrated adj circuit adj chip) or IC adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:29
L4	1	1 and batter\$3 and ((integrated adj circuit adj chip) or IC adj chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:31
L5	1	1 and batter\$3 and ((integrated adj circuit adj chip) or (IC adj chip) or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:31
L6	2	2 and batter\$3 and ((integrated adj circuit adj chip) or (IC adj chip) or chip)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/01/27 15:31